



0941.70218

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re U.S. Patent Application

Applicant: Mastsui et al.

Serial No. 10/811,769

Conf. No. 9515

Filed: March 29, 2004

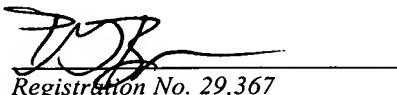
For: THIN FILM TRANSISTOR ARRAY
SUBSTRATE AND METHOD FOR
REPAIRING THE SAME

Art Unit: 2814

Examiner: Unassigned

I hereby certify that this paper is being deposited with
the United States Postal Service as FIRST-CLASS mail
in an envelope addressed to: Mail Stop MISSING
PARTS, Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, on this date.

July 12, 2004

Date
F-CLASS.WCM
Appr. February 20, 1998
Registration No. 29,367

Attorney for Applicant

CLAIM FOR PRIORITY

Mail Stop MISSING PARTS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants claim foreign priority benefits under 35 U.S.C. § 119 on the
basis of the foreign applications identified below:

Japanese Patent Application No. 2003-097104, filed March 31, 2003
Japanese Patent Application No. 2004-066096, filed March 9, 2004

Certified copies of the priority documents are enclosed.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By



Patrick G. Burns
Registration No. 29,367

Customer No. 24978

July 12, 2004
300 South Wacker Drive
Suite 2500
Chicago, Illinois 60606
Phone: (312) 360-0080
Fax: (312) 360-9315